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**SCULLY, SCOTT, MURPHY
& PRESSER**

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Fax

To: Examiner Tuan T. Lam
Group Art Unit 2816
Confirm. No. 4656

From: Paul J. Esatto, Jr.

Fax: 703-872-9306

Pages: 3 (incl. Cover page)

Phone:

Date: July 14, 2004

Re: U.S. Patent Appn.
Serial No. 10/669,304
Yoji Idei, et al.
Our Docket: 17051

CC:

☐ Urgent ☒ For Review ☐ Please Comment ☐ Please Reply ☐ Please Recycle

• **Comments:**

Enclosed are the following documents:

Supplemental Information Disclosure Statement; together with PTO-1449 to correct a typographical error.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**Applicant(s):** Yoji Idei, et al.**Examiner:** Tuan T. Lam**Serial No:** 10/669,304**Art Unit:** 2816**Filed:** September 24, 2003RECEIVED
CENTRAL FAX CENTER**Docket:** 17051**For:** SEMICONDUCTOR DEVICE
USING CURRENT MIRROR
CIRCUIT

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Dated: July 14, 2004


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Confirmation No. 4656Mailstop Issue Fees
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

In connection with the above-identified patent application, and supplemental to applicant's Information Disclosure Statement dated September 24, 2003, enclosed is a new PTO-1449 form to correct a typographical error thereon wherein the correct date of Japanese Unexamined Patent Publication 11-242204 should be September 7, 1999. Acknowledgement and entry of the same is requested.

Respectfully submitted,


Paul J. Esatto, Jr.
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PJE:ac**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: July 14, 2004
Paul J. Esatto, Jr.

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